MICROPROCESSORS
4th Exam/ECE/2595/Jun'2022
(For 2018 Batch Onwards)

Duration: 3Hrs.   **M.Marks:75**

SECTION-A

Q1. Do as directed.   **15x1=15**
   a. SIM stands for _____.
   b. DAA stands for_____.
   c. EPROM stands for_____
   d. PSW stands for _____.
   e. ALE stands for _____.
   f. A nibble is of _____ bits.
   g. Program Counter is a _____ bit register.
   h. MVI A, 2C H is a _____ byte instruction.
   i. _____ is highest priority Interrupt.
   j. 8085 has _____ data lines.
   k. Stack works on Last In _____ Out principle.
   l. Program Counter (PC) points to the next instruction to be executed. (T/F)
   m. Accumulator in 8085 is 16 bit register. (T/F)
   n. 8085 has 8 bit Address Bus. (T/F)
   o. Flag register of 8085 has 5 flags. (T/F)

SECTION-B

Q2. Attempt any six questions.   **6x5=30**
   i. Explain the function of Program counter.
   ii. Discuss Flag register of 8085.
   iii. Differentiate between I/O mapped I/O and Memory mapped I/O.
   iv. Explain the function of PUSH and POP instructions.
   v. Briefly discuss the working of 8255 chip.
   vi. Explain various steps involved in program execution.
   vii. Define Address bus, Data bus and Control bus.

SECTION-C

Q3. Attempt any three questions.   **3x10=30**
   a. Draw and explain the Functional Block diagram of 8085.
   b. Discuss the evolution and applications of microprocessors.
   c. Discuss various addressing modes of 8085 with examples.
   d. Write a short note on the following.   i) Instruction set of 8085   ii) Subroutine
   e. Explain the following.   i) Stack and Stack Pointer   ii) Functions of ALE