

S. B. Roll. No.....

MICROPROCESSORS
4th Exam/ECE/2595/Dec'22
(For 2018 Batch Onwards)

Duration: 3Hrs.

M.Marks:75

SECTION-A

Q1. Answer the following questions:

15x1=15

- a. Define the term Fetch cycle.
- b. How many total address lines in 8085?
- c. Which is a non-maskable restart interrupt?
- d. How many general purposes register are there in 8085?
- e. How many Interrupts are in 8085?
- f. Define word.
- g. Define byte.
- h. What is microprocessor?
- i. SIM stands for_____
- j. Which is the lowest priority interrupt in 8085?
- k. Explain the function of ALE pin in 8085.
- l. What is the size of stack pointer in 8085?
- m. What is the function of SID pin in 8085?
- n. The accumulator is an _____bit register in 8085.
- o. HLDA stands for_____

SECTION-B

Q2. Attempt any six questions.

6x5=30.

- i. Explain push and pop instruction.
- ii. What is an interrupt? Differentiate between hardware and software interrupts.
- iii. What is the difference between accumulator and other general-purpose registers?
- iv. What are status flags? Explain their importance.
- v. Briefly explain the organization of a microcomputer system.
- vi. Write short note on DMA.
- vii. Differentiate between I/O mapped I/O and memory mapped I/O.
- viii. Explain the block diagram of 8085.
- ix. Explain the use of HOLD and HLDA pins in 8085.

SECTION-C

Q3. Attempt any three questions.

3x10=30

- a. Draw and explain the architecture of 8085 in detail?
- b. Discuss various addressing modes of 8085.
- c. Give the pin diagram of 8085 and explain function of each pin.
- d. What are the various data transfer schemes? Explain handshaking scheme in detail.
- e. Write short on:
i) 8251 communication interface ii) 8253 PIT