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## PLCs & SCADA 5<sup>th</sup> Exam/ECE/4617/Dec'22 (For 2018 batch onwards)

Duratio	on: 3Hrs.	M. Marks: 75
	SECTION-A	
a. b. c. d. e. f. g. h. i. j. k.	SECTION-A  D as directed: PLC stands for How many timers are used in PLC? What is XIC? What is Watch Dog Timer? SCADA stands for RAM stands for DCS stands for LD stands for LD stands for What is OTE? CPU stands for PROM stands for	15x1=15
	LIM stands for	
n.	PLC scan cycle divide intosteps.	
0.	Write any one bit instruction.	
	SECTION-B	
Q2. Att	tempt any six questions.	6x5=30
	Explain the memory organization of PLC.	S 50
	What is counter? How many counters are used in PLC?	
iii.	Explain all Comparison instructions.	
	Explain in detail watch dog timer.	
	What are the advantages of PLC over Relays?	
	Explain PLC operation in detail.	
	Explain DCS in detail.	
viii.	Explain any two input instructions.	
O2 44	SECTION-C	2::10, 20
	tempt any three questions.	3x10=30
	What is SCADA? Explain the block diagram of SCADA.	
	· ·	
c. d.	What is timer? Explain all timer instructions  Explain all bit instructions in detail.	
e.		
G.	Z. I. S.	